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processing, multicore processors and FPGAs are blurring the lines between hardware and software and fundamentally altering the way digital design and design logic should be taught. Vahid's RTL-early

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approach better assists
students in grasping the
essential design
fundamentals before digging
into specific details of
design optimization.

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Register Files or Memories

265. RTL Design Pitfall

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Involving Storage Updates
271. RTL Design Involving a
Timer 272. A Data-Dominated
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Determining Clock Frequency
278. 5.6 Behavioral-Level
Design: C to Gates
(Optional) 281

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...

In digital circuit design,
register-transfer level is a
design abstraction which
models a synchronous digital

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circuit in terms of the flow of digital signals between hardware registers, and the logical operations performed on those signals. Register-transfer-level abstraction is used in hardware description languages like

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Verilog and VHDL to create high-level representations of a circuit, from which lower-level representations and ultimately actual wiring can be derived. Design at the RTL level

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~~Register transfer level~~

~~Wikipedia~~

A student exposed to RTL design in a first course will have a more relevant view of the modern digital design field, leading not only to a better

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Appreciation of modem computers and other digital devices, but to a more accurate index Preface understanding of careers involving digital design.

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Offering a modern, updated

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approach to digital design,
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noting a clear distinction
between design and gate-
level minimization.

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RTL Design: The only major

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digital design book to emphasize RTL (register-transfer-level) design, central to the million-gate IC era, while continuing to introduce topics fully bottom-up. Teaches modern "Capture/Convert" top-down

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design methodology for
combinational, sequential,
and RTL design.

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approach to digital design,
whereas most literature
available is sorely outdated
Progresses though low levels
of design, making a clear
distinction between design

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and gate-level minimization
Addresses the various uses
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Register Transfer Level
(RTL) is an abstraction for
defining the digital
portions of a design. It is
the principle abstraction

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Used for defining electronic systems today and often serves as the golden model in the design and verification flow. The RTL design is usually captured using a hardware description language (HDL) such as

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(vii) Digital Design with
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into specific details of
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Logic design is a step in
the standard design cycle in
which the functional design

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of an electronic circuit is converted into the representation which captures logic operations, arithmetic operations, control flow, etc. A common output of this step is RTL description. Logic design is

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commonly followed by the
circuit design step.

~~Logic synthesis — Wikipedia~~
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To Study Digital Design ix
To Instructors of Digital
Design ix How to Use This
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Us 1 1.2 The World of ...

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Chapter 1: - Introduction.

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Verilog...~~

Frank Vahid is the author of

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